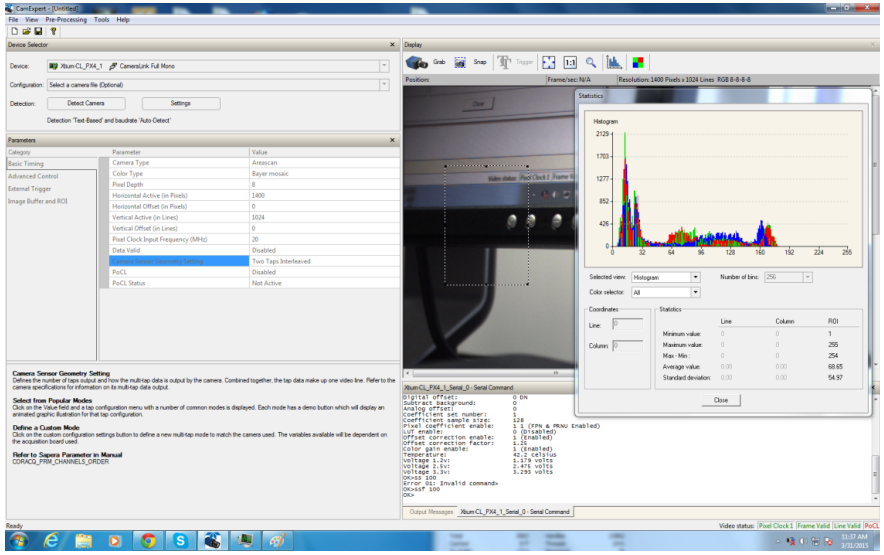
The Next Generation of Frame Grabber

Introducing the Xtium2-CLHS FX8 LC, the newest member of the Xtium2 CLHS family. The Xtium2 series is designed to support all of the frame grabber based machine vision camera standards - Camera Link HS®, CoaXPress®, and Camera Link®. The Xtium2-CLHS FX8 LC features two 10Gbit/s SFP+ inputs to deliver 2GByte/sec sustained host bandwidth.

The Xtium2-CLHS FX8 LC supports up to two bidirectional SFP+ modules running at 10 Gbit/s. Using CLHS X-Protocol, the Xtium2-CLHS FX8 delivers up to 2.0 GB/s of image acquisition and 6.8 GB/s to host memory. The low-cost SFP+ module and fiber optic cables help extend cable lengths to 100 meters.

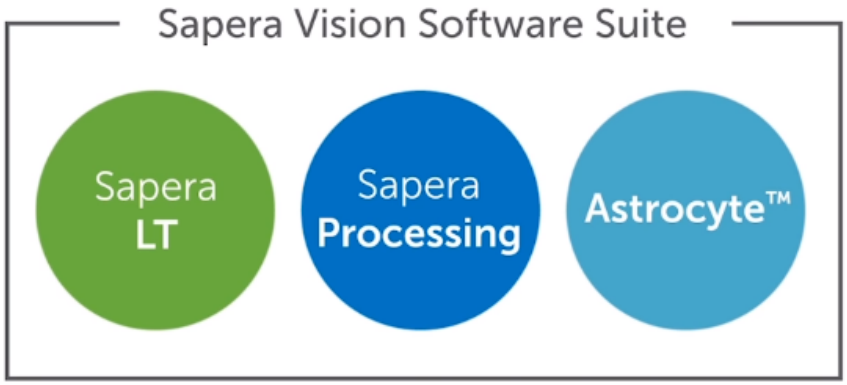


Free Acquisition and Control Software Libraries

The Xtium and Xtium2 series of frame grabbers are fully supported by Sapera LT SDK. Sapera LT SDK is an image acquisition and control software development toolkit (SDK) for Teledyne DALSA's cameras and frame grabbers. Hardware independent by nature, Sapera LT offers a rich development ecosystem for machine vision OEMs and system integrators. Sapera LT SDK supports image acquisition from cameras and frame grabbers based on standards including GigE Vision™, Camera Link®, CoaXPress®, and Camera Link HS™.

Fully supported by Sapera™ Vision SDK

When combined with a compatible Teledyne DALSA frame grabber, standard Sapera Processing run-time licenses are offered at no additional charge. Sapera Processing is at the heart of Sapera Vision Software, delivering a suite of image processing and analysis functions. These functions include over 400 image processing primitives, barcode tools, pattern matching tools (both area-based and edge-based), OCR, color and blob analysis, measurement, and calibration tools for perspective and lens correction. The standard tools run-time license includes access to image processing functions, area based (normalized correlation based) template matching tools, blob analysis, and lens correction tools.



Specifications

Part Number	OR-A8S0-FX820
Bandwidth	Input CHLS camera: up to 5 GB/s in frame grabber memory PCIe bus output: up to 7.0 GB/sec sustained (PCIe payload @ 512 bytes) PCIe bus output: up to 6.8 GB/sec sustained (PCIe payload* @ 256 bytes)
Board Type	PCIe
Host Bus	PCI Express Gen3 x8
Board Interface	Camera Link HS
Connectors	Data input: 2 x SFP+ cages Data forward: shared Camera control I/O: 1 x DH60-27P (main bracket), 1 x 26-pin shrouded header Multi-board sync: 1 x 16-pin shrouded connector
Camera Format	CLHS X-protocol (64 b/66 b encoding): up to 2-lanes @ 10.3125 Gb/s
Transmission Rate	Up to 2-lanes x 10.3125 Gbps (20.625 Gbps total)
Bits Per Pixel	Mono: 8, 10, 12-bit/pixel RGB: 8-bit/pixel/color (no alignment)
Camera Control	External trigger input, strobe out, quadrature encoder, multi-board sync, camera control through Genicam, bit-error detection and correction

GPIO	4x opto-couple (2 shared with external trigger) 8 LVTTL outputs (1 shared with strobe)
Frame Buffer	512 MB on-board memory (shared with processing function)
Features	Data-forwarding for distributed image processing, multi-board synchronization: grab images from multiple independent cameras in one image buffer, long cable lengths (fiber), supports T2IR (Trigger2Image Reliability) framework
Software	Sapera LT SDK
OS Support	Windows 11 (64-bit), Windows 10 (64-bit), Windows 10 (32-bit) through WOW64, Linux (64-bit)
Input Camera Port	up to 2 cameras