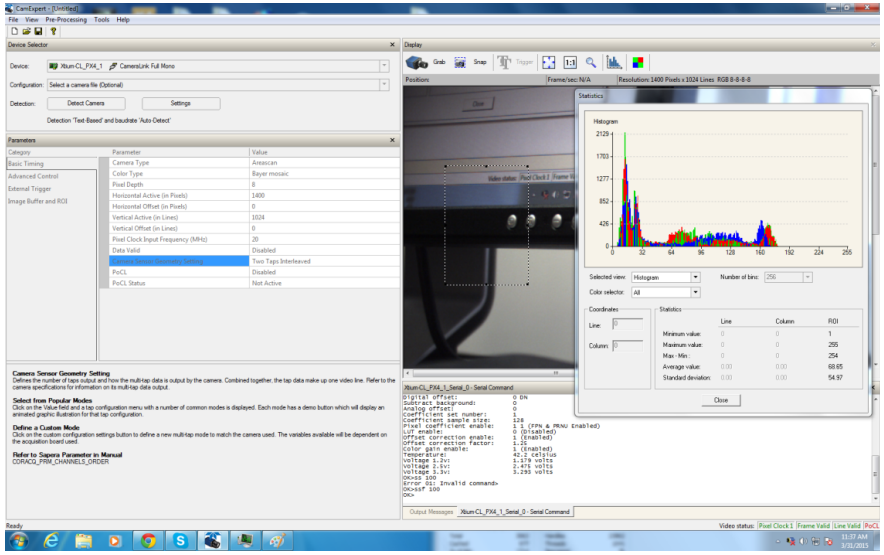
The Next Generation of Frame Grabbers

The Xtium™ 2 CLHS PX8 features Camera Link HS standard on the PCI Express™ Gen 3.0 platform. This single cable, single slot solution supports up to 4-CLHS lanes, each operating at 10.3 Gbits/s, to acquire images at up to 5.0 GBytes/s and transfer them using PCIe x8 slots to the host memory. The Xtium2-CLHS supports Active Optical Cables (AOC) to extend the cable length beyond 100 meters. Built-in data-forwarding and multi-board sync capabilities deliver a platform for distributed processing for high-speed, computational intensive machine vision applications.

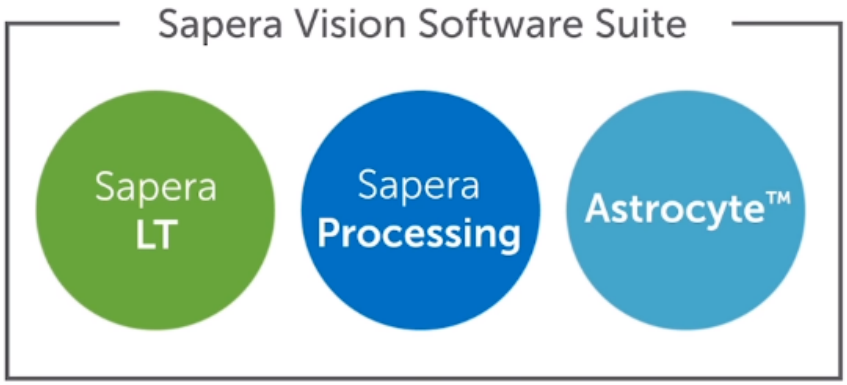


Free Acquisition and Control Software Libraries

The Xtium and Xtium2 series of frame grabbers are fully supported by Sapera LT SDK. Sapera LT SDK is an image acquisition and control software development toolkit (SDK) for Teledyne DALSA's cameras and frame grabbers. Hardware independent by nature, Sapera LT offers a rich development ecosystem for machine vision OEMs and system integrators. Sapera LT SDK supports image acquisition from cameras and frame grabbers based on standards including GigE Vision™, Camera Link®, CoaXPress®, and Camera Link HS™.

Fully supported by Sapera™ Vision SDK

When combined with a compatible Teledyne DALSA frame grabber, standard Sapera Processing run-time licenses are offered at no additional charge. Sapera Processing is at the heart of Sapera Vision Software, delivering a suite of image processing and analysis functions. These functions include over 400 image processing primitives, barcode tools, pattern matching tools (both area-based and edge-based), OCR, color and blob analysis, measurement, and calibration tools for perspective and lens correction. The standard tools run-time license includes access to image processing functions, area based (normalized correlation based) template matching tools, blob analysis, and lens correction tools.



Specifications

Part Number	OR-A8S0-PX840
Bandwidth	Input CHLS camera: up to 5.0GB/s in frame grabber memory PCIe bus output: up to 7.0GB/sec sustained (PCIe payload @ 512 bytes) PCIe bus output: up to 6.8GB/sec sustained (PCIe payload* @ 256 bytes)
Board Type	PCIe
Host Bus	PCI Express Gen3 x8
Board Interface	Camera Link HS
Connectors	Data input: 1 x CX4 thumbscrew, AOC ready Data forward: 1 x CX4 thumbscrew, AOC ready Camera control I/O: 1 x DH60-27P (main bracket), 1 x 26-pin shrouded header Multi-board sync: 1 x 16-pin shrouded connector
Camera Format	CLHS X-protocol (64 b/66 b encoding): up to 4-lanes @ 10.3125 Gb/s
Transmission Rate	4-lanes x 10.3125 Gbps (41.25 Gbps total)
Bits Per Pixel	Mono: 8, 10, 12-bit/pixel RGB: 8-bit/pixel/color (no alignment)
Camera Control	External trigger input, strobe out, quadrature encoder, multi-board sync,

	camera control through Genicam, bit-error detection and correction
GPIO	4x opto-couple (2 shared with external trigger) 8 LVTTL outputs (1 shared with strobe)
Frame Buffer	512 MB (shared with processing function)
Features	Data-forwarding for distributed image processing, multi-board synchronization: grab images from multiple independent cameras in one image buffer, long cable lengths (AOC), supports T2IR (Trigger2Image Reliability) framework
Software	Sapera LT SDK
OS Support	Windows 11 (64-bit), Windows 10 (64-bit), Windows 10 (32-bit) through WOW64, Linux (64-bit)
Input Camera Port	1 camera